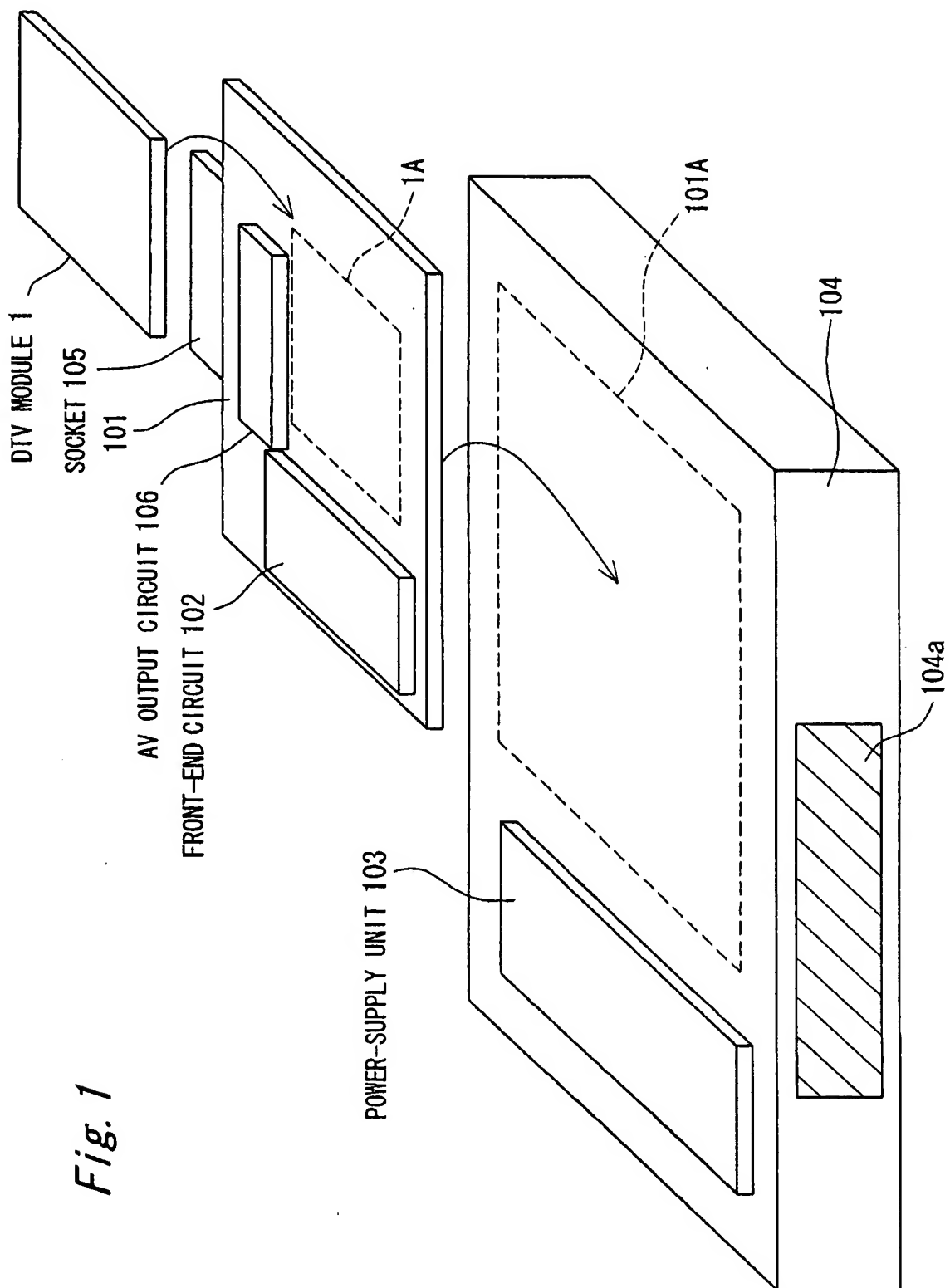
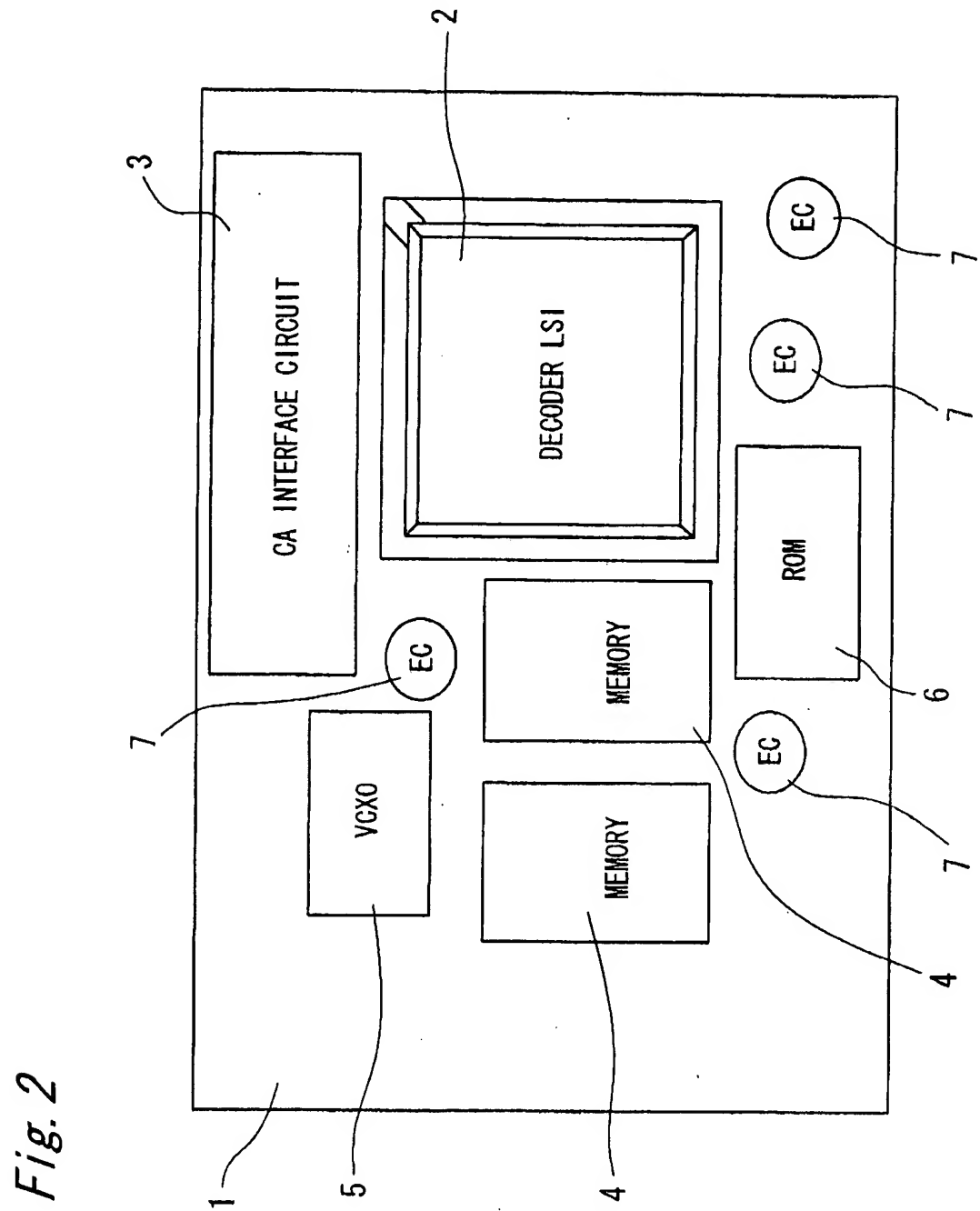


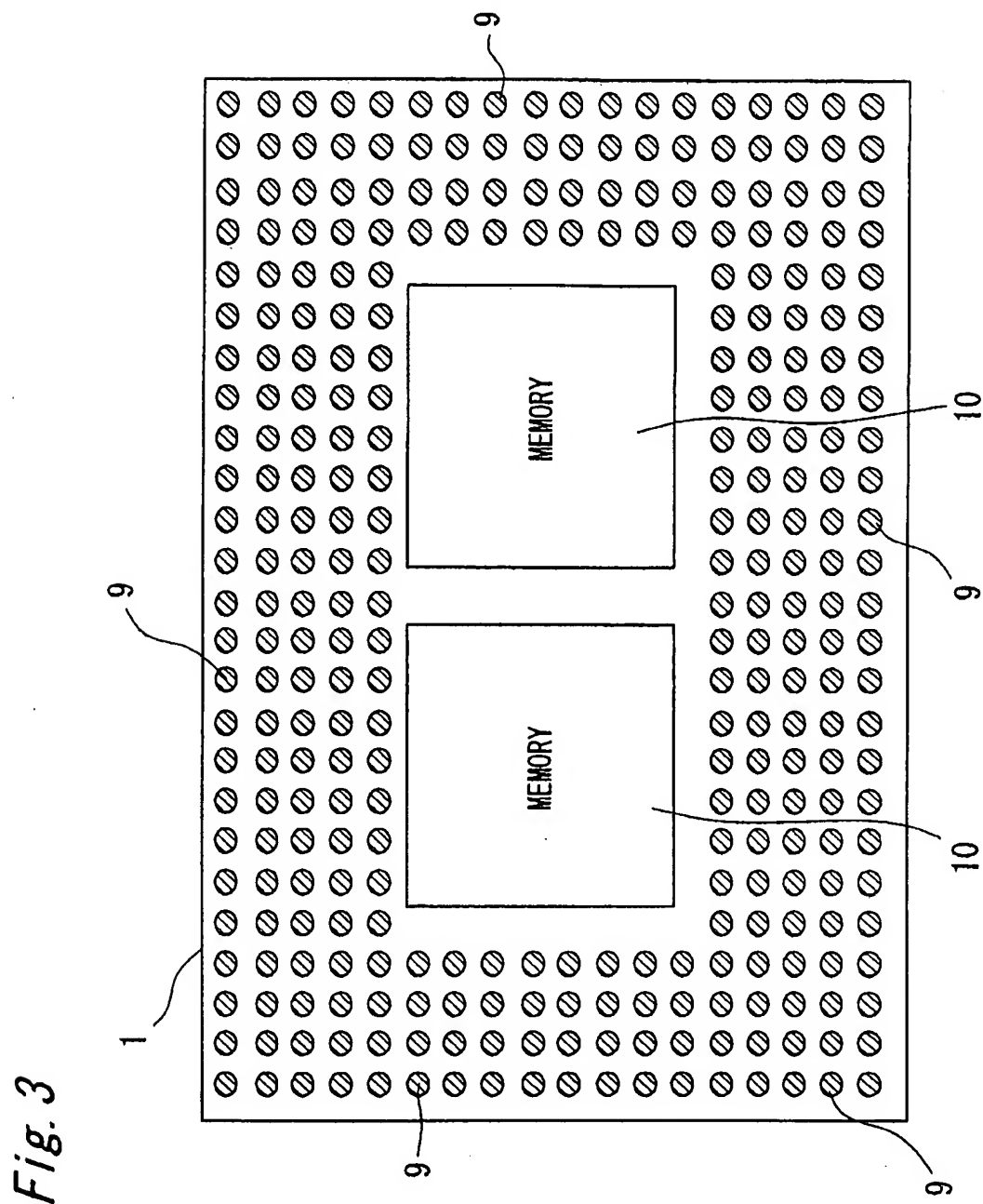
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*Fig. 1*

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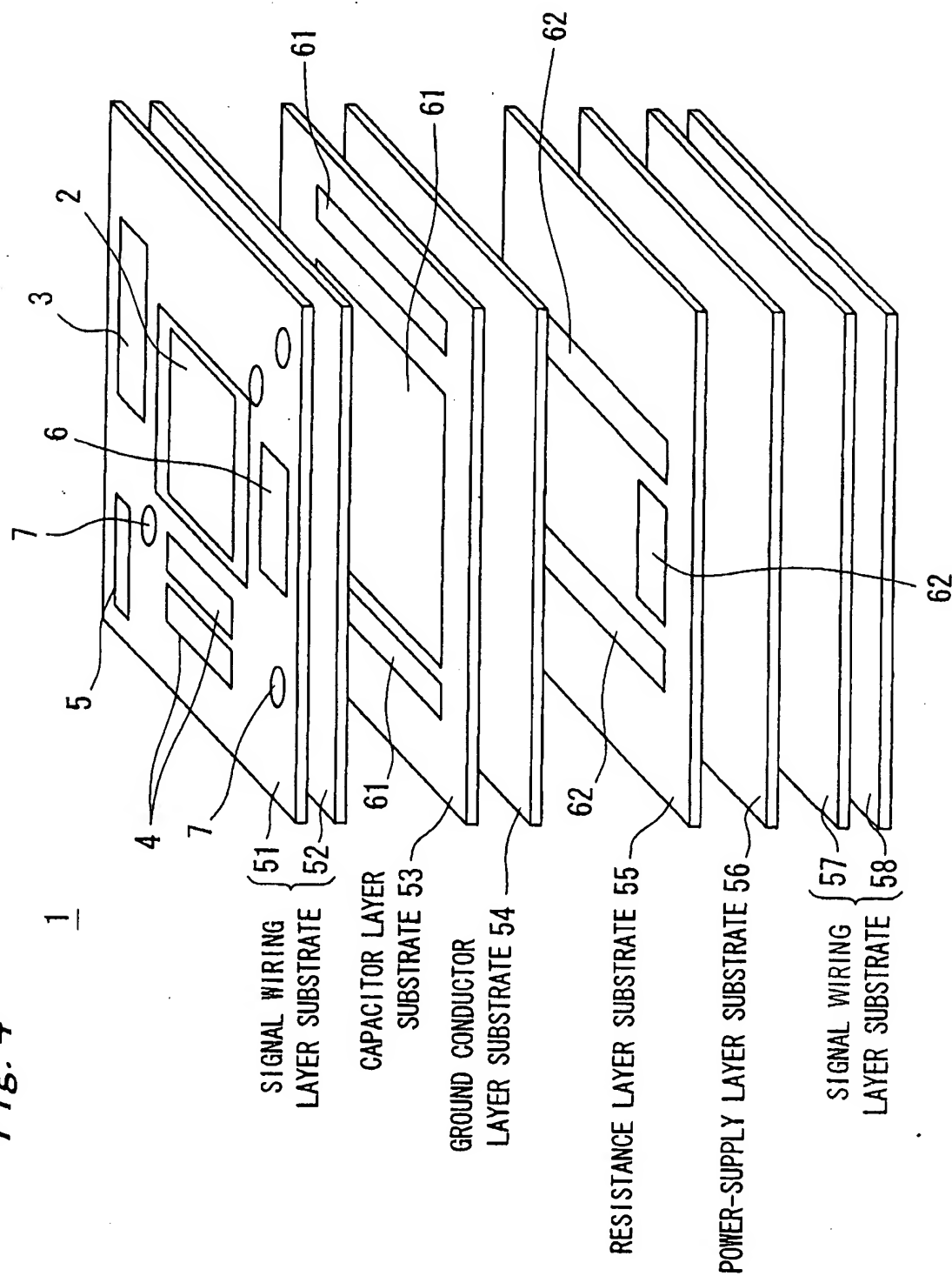


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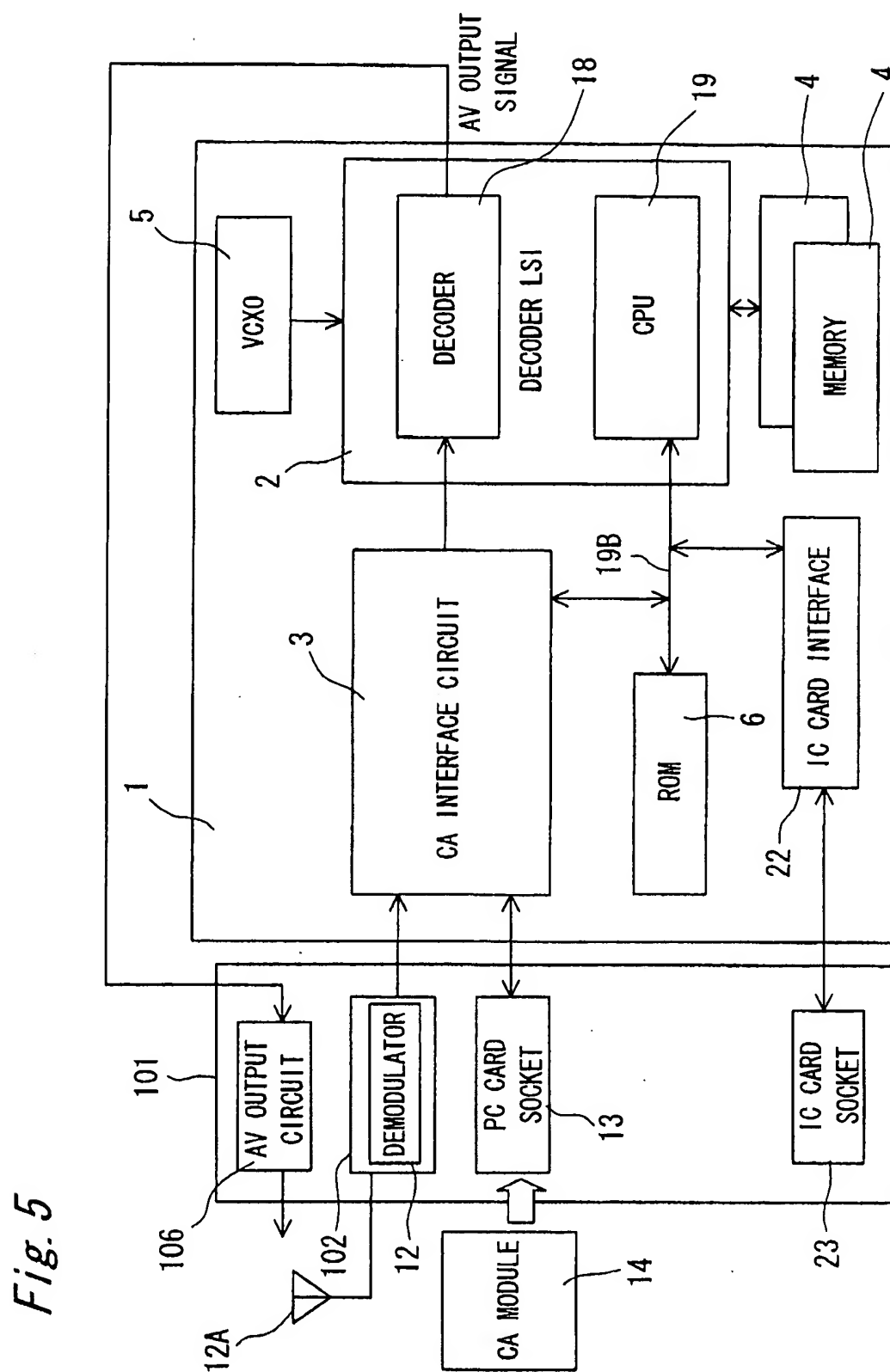


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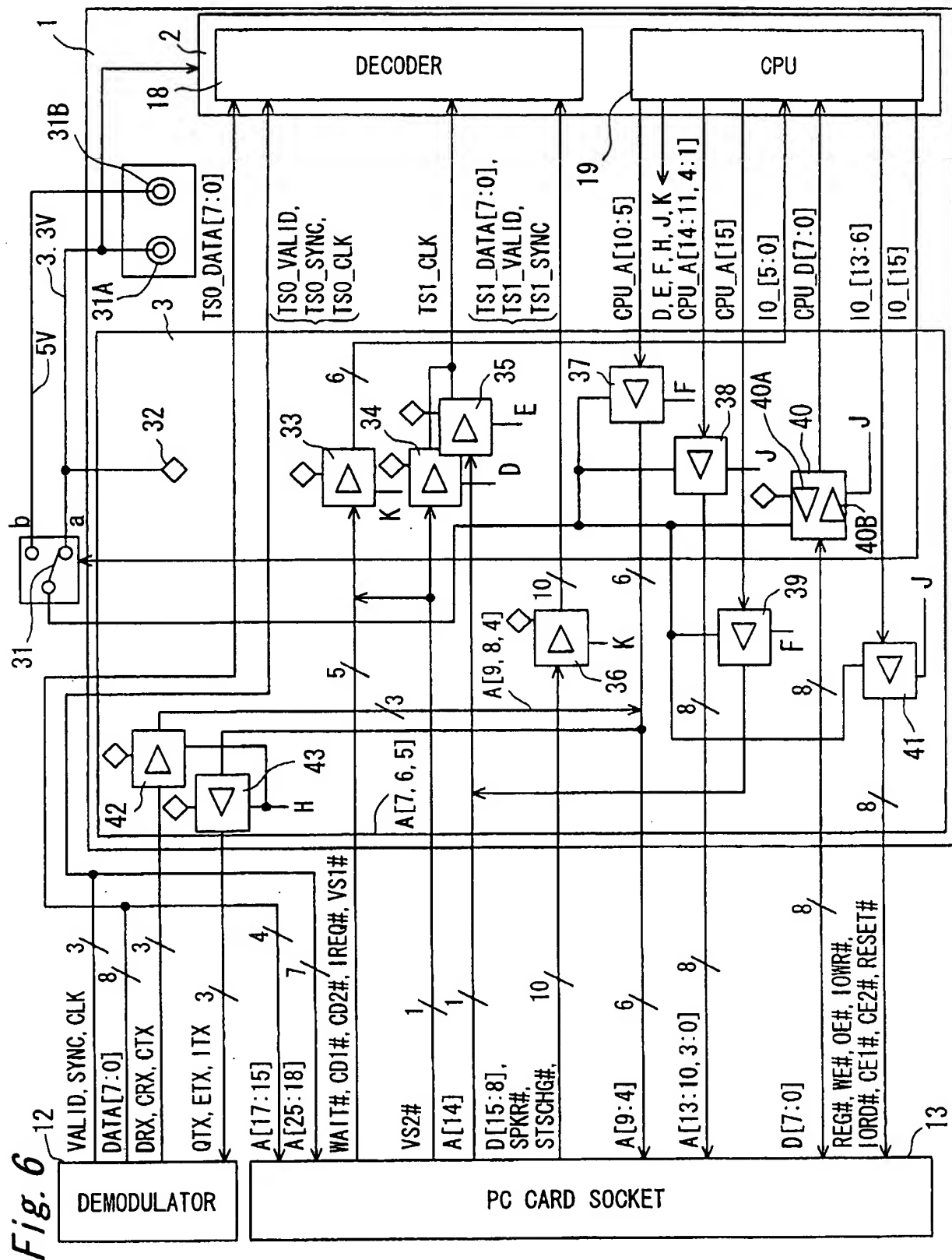
Fig. 4



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Fig. 7

	WHEN CA MODULE 14 IS NOT INSERTED	WHEN CA MODULE 14 IS INSERTED		
		CI	CABLE CARD	
			INITIAL STATE	OPERATING STATE
ENABLE CONTROL SIGNAL	D	OFF	ON	OFF
	E	OFF	OFF	ON
	F	OFF	ON	OFF
	H	OFF	OFF	ON
	J	OFF	ON	ON
	K	ON	ON	ON

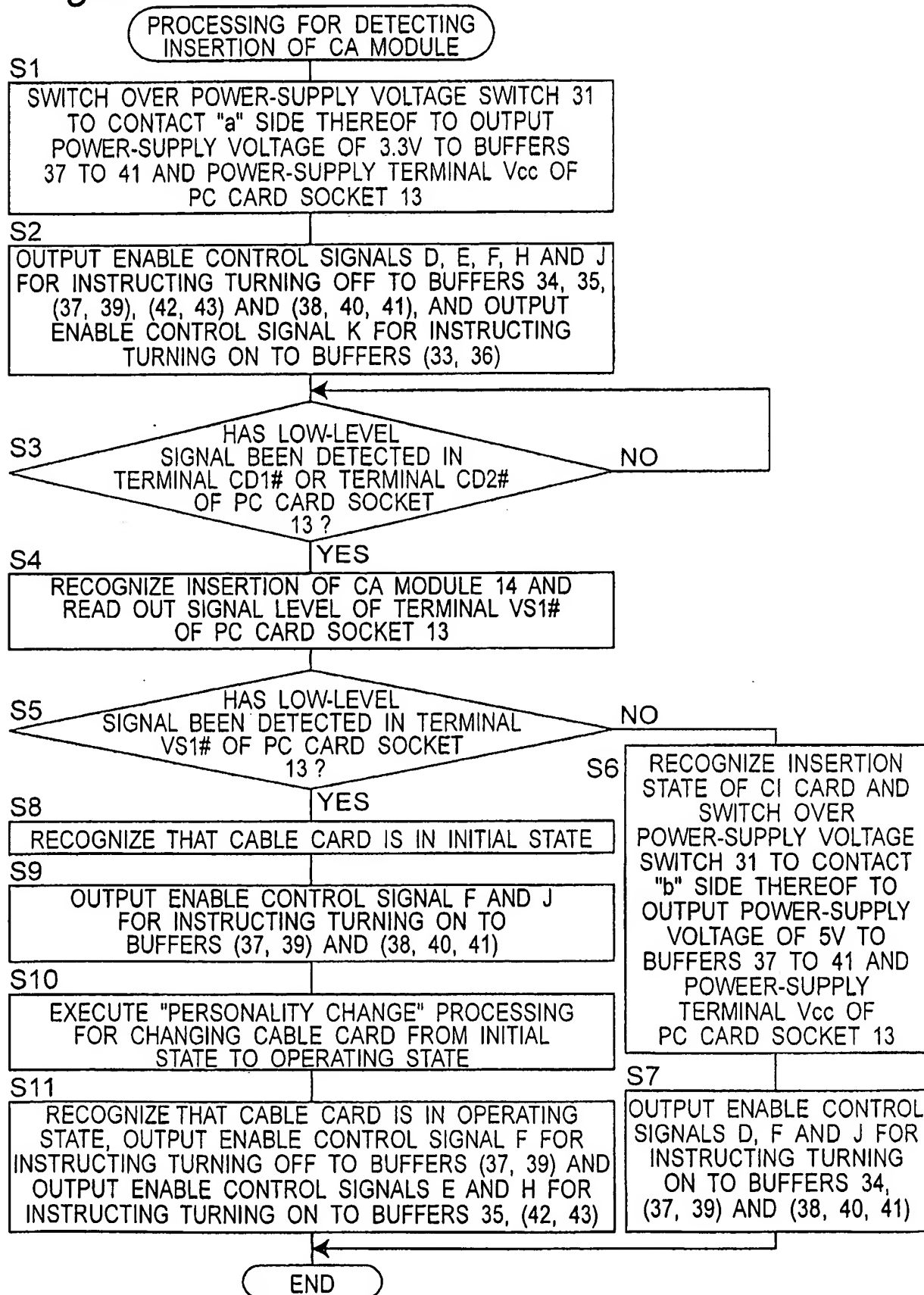
Fig. 8

	WHEN CA MODULE 14 IS NOT INSERTED	WHEN CA MODULE 14 IS INSERTED		
		CI	CABLE CARD	
			INITIAL STATE	OPERATING STATE
BUFFER POWER SUPPLY AND PC CARD POWER SUPPLY V <sub>CC</sub>	3.3V	5V	3.3V	3.3V

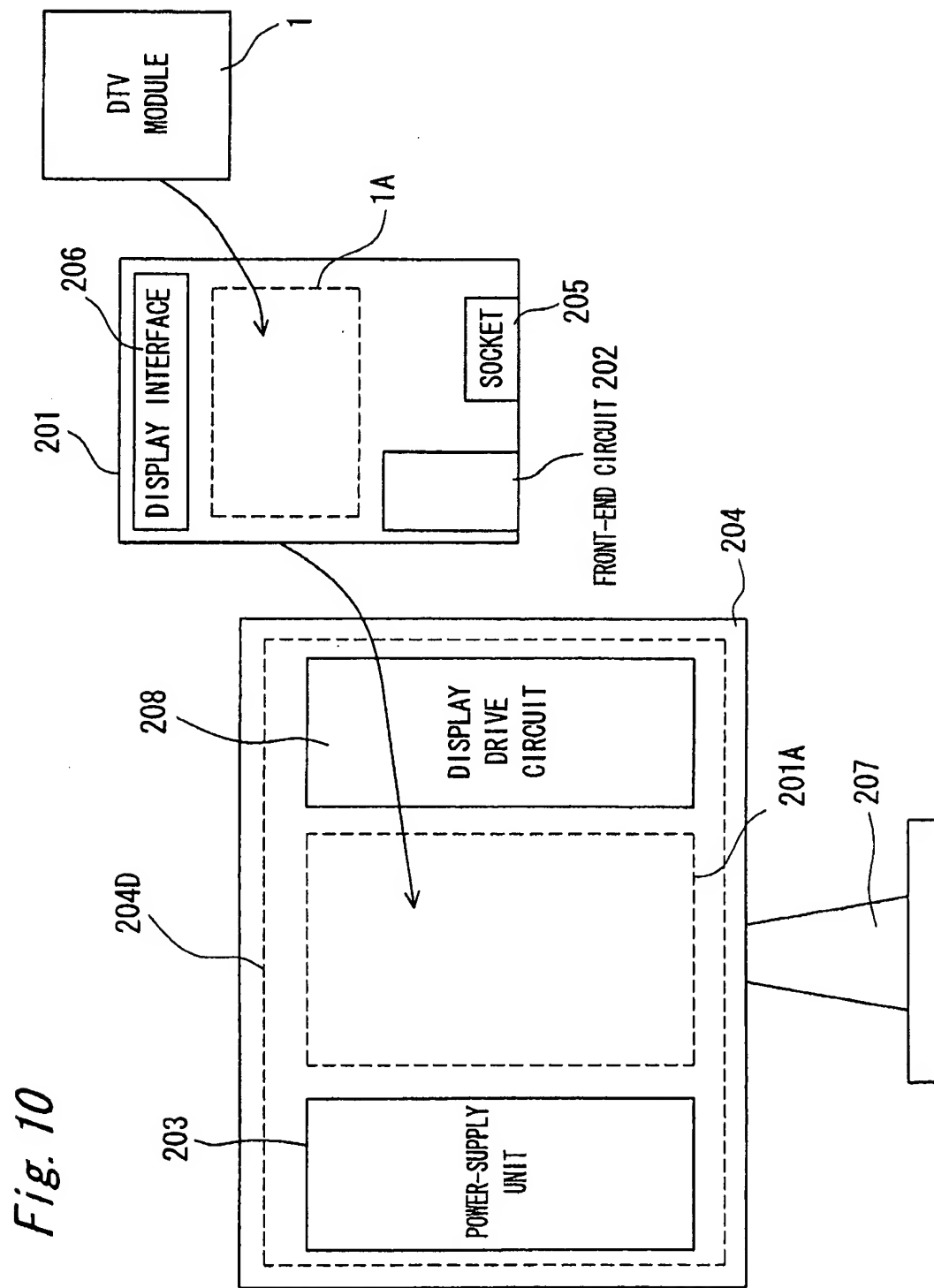


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Fig. 9

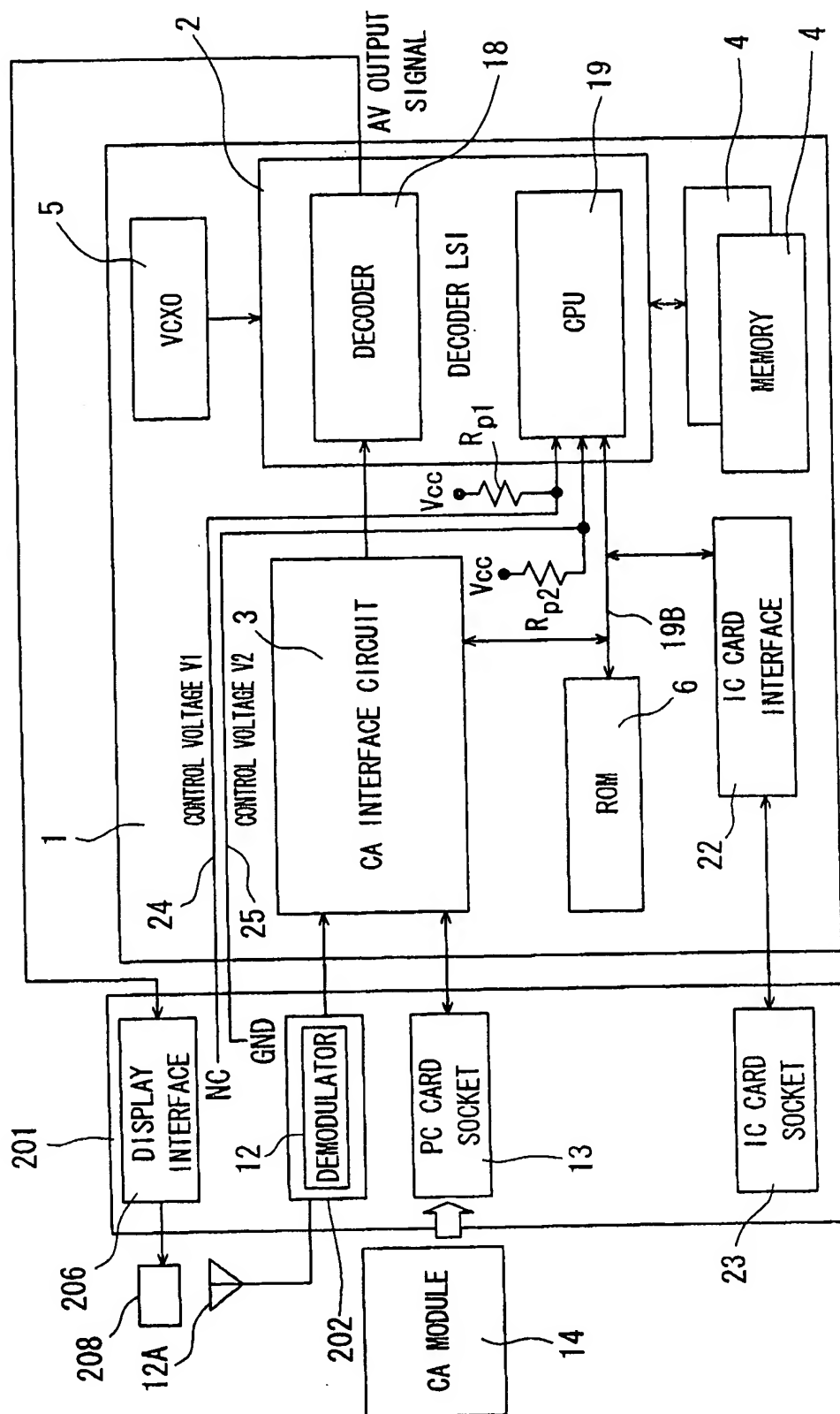


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Fig. 11



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*Fig. 12*

		CONTROL VOLTAGE V1	
		0	1
CONTROL VOLTAGE V2	0	ISDB-T IN JAPAN	OPEN CABLE IN NORTH AMERICA
	1	DVB-T IN EUROPE	NO MOTHERBOARD

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Fig. 13

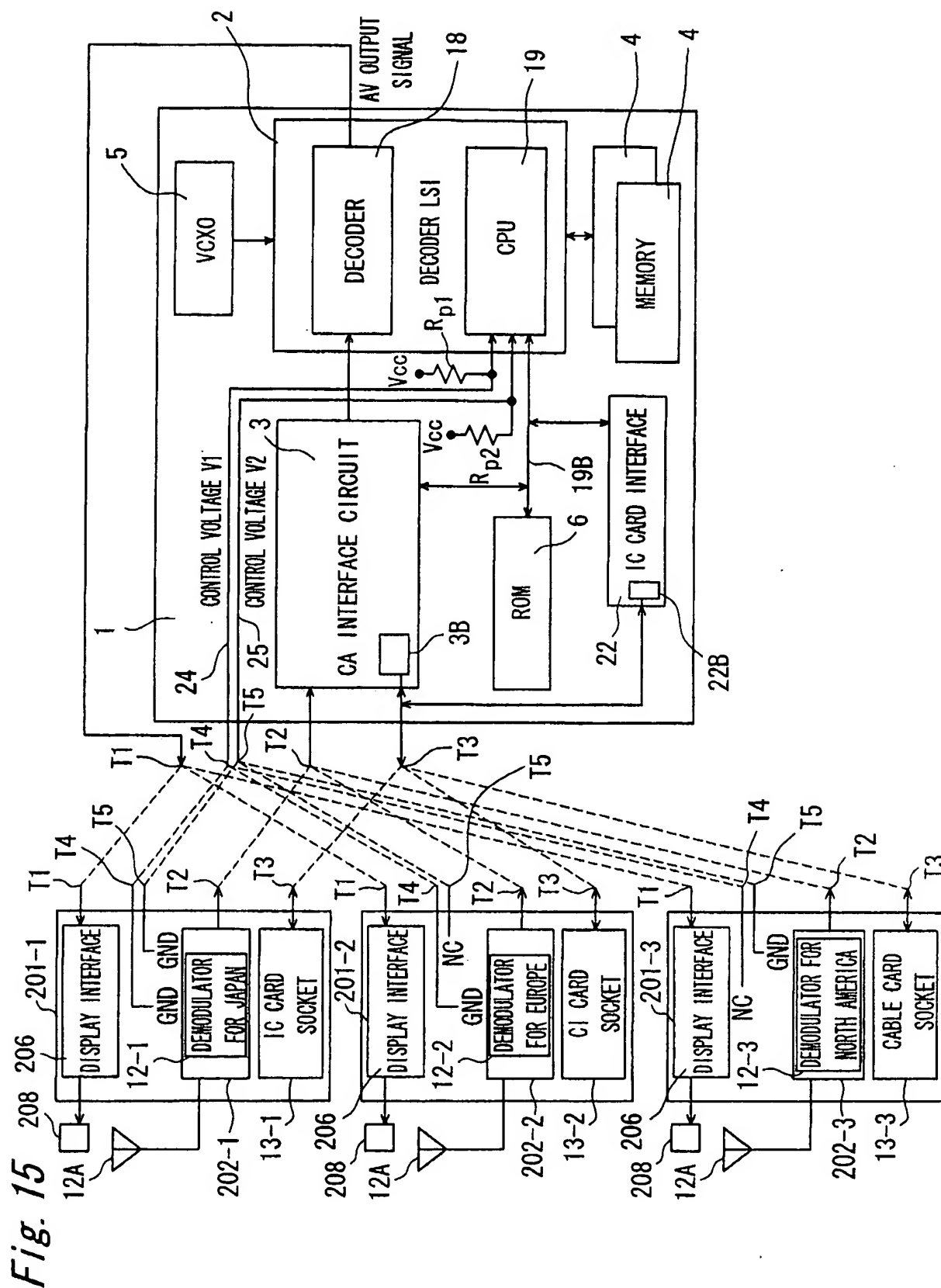
	ISDB-T IN JAPAN	DVB-T IN EUROPE	OPEN CABLE IN NORTH AMERICA	
			INITIAL STATE	OPERATING STATE
ENABLE CONTROL SIGNAL	D	OFF	ON	OFF
	E	OFF	OFF	ON
	F	OFF	ON	OFF
	H	OFF	OFF	ON
	J	OFF	ON	ON
	K	ON	ON	ON

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Fig. 14

	ISDB-T IN JAPAN	DVB-T IN EUROPE	OPEN CABLE IN NORTH AMERICA	
			CABLE CARD	
			INITIAL STATE	OPERATING STATE
BUFFER POWER SUPPLY AND PC CARD POWER SUPPLY V <sub>CC</sub>	3.3V	5V	3.3V	3.3V

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ISDB-T IN JAPAN	DVB-T IN EUROPE	OPEN CABLE IN NORTH AMERICA	
		INITIAL STATE OF CABLE CARD	OPERATING STATE OF CABLE CARD
NOT IN USE	CPU DATA[3]	CPU DATA[3]	CPU DATA[3]
NOT IN USE	CPU DATA[4]	CPU DATA[4]	CPU DATA[4]
NOT IN USE	CPU DATA[5]	CPU DATA[5]	CPU DATA[5]
NOT IN USE	CPU DATA[6]	CPU DATA[6]	CPU DATA[6]
NOT IN USE	CPU DATA[7]	CPU DATA[7]	CPU DATA[7]
NOT IN USE	CE1#	CE1#	CE1#
NOT IN USE	CPU ADDRESS[10]	CPU ADDRESS[10]	NOT IN USE
NOT IN USE	OE#	OE#	OE#
NOT IN USE	CPU ADDRESS[11]	CPU ADDRESS[11]	NOT IN USE
NOT IN USE	CPU ADDRESS[9]	CPU ADDRESS[9]	DRX
NOT IN USE	CPU ADDRESS[8]	CPU ADDRESS[8]	CRX
NOT IN USE	CPU ADDRESS[13]	CPU ADDRESS[13]	NOT IN USE
NOT IN USE	CPU ADDRESS[14]	CPU ADDRESS[14]	TS OUT CLOCK
NOT IN USE	WE#	WE#	WE#
NOT IN USE	IREQ#	READY	IREQ#
NOT IN USE	TS IN VALID	CPU ADDRESS[16]	TS IN VALID
NOT IN USE	TS IN CLOCK	CPU ADDRESS[15]	TS IN CLOCK
NOT IN USE	CPU ADDRESS[12]	CPU ADDRESS[12]	NOT IN USE
NOT IN USE	CPU ADDRESS[7]	CPU ADDRESS[7]	QTX
NOT IN USE	CPU ADDRESS[6]	CPU ADDRESS[6]	ETX
NOT IN USE	CPU ADDRESS[5]	CPU ADDRESS[5]	ITX
NOT IN USE	CPU ADDRESS[4]	CPU ADDRESS[4]	CTX
NOT IN USE	CPU ADDRESS[3]	CPU ADDRESS[3]	NOT IN USE
NOT IN USE	CPU ADDRESS[2]	CPU ADDRESS[2]	NOT IN USE

Fig. 16



ISDB-T IN JAPAN	DVB-T IN EUROPE	OPEN CABLE IN NORTH AMERICA	
		INITIAL STATE OF CABLE CARD	OPERATING STATE OF CABLE CARD
NOT IN USE	CPU ADDRESS[1]	CPU ADDRESS[1]	CPU ADDRESS[1]
NOT IN USE	CPU ADDRESS[0]	CPU ADDRESS[0]	CPU ADDRESS[0]
NOT IN USE	CPU DATA[0]	CPU DATA[0]	CPU DATA[0]
NOT IN USE	CPU DATA[1]	CPU DATA[1]	CPU DATA[1]
NOT IN USE	CPU DATA[2]	CPU DATA[2]	CPU DATA[2]
NOT IN USE	IOIS16#	WP	IOIS16#
NOT IN USE	CD1#	CD1#	CD1#
NOT IN USE	TS OUT DATA[3]	CPU DATA[11]	TS OUT DATA[3]
NOT IN USE	TS OUT DATA[4]	CPU DATA[12]	TS OUT DATA[4]
NOT IN USE	TS OUT DATA[5]	CPU DATA[13]	TS OUT DATA[5]
NOT IN USE	TS OUT DATA[6]	CPU DATA[14]	TS OUT DATA[6]
NOT IN USE	TS OUT DATA[7]	CPU DATA[15]	TS OUT DATA[7]
NOT IN USE	CE2#	CE2#	CE2#
NOT IN USE	VS1#	VS1#	VS1#
NOT IN USE	IORD#	RESERVED	IORD#
NOT IN USE	IOWR#	RESERVED	IOWR#
NOT IN USE	TS IN SYNC	CPU ADDRESS[17]	TS IN SYNC
NOT IN USE	TS IN DATA[0]	CPU ADDRESS[18]	TS IN DATA[0]
NOT IN USE	TS IN DATA[1]	CPU ADDRESS[19]	TS IN DATA[1]
RESET2 (OPTION)	TS IN DATA[2]	CPU ADDRESS[20]	TS IN DATA[2]
CLOCK2 (OPTION)	TS IN DATA[3]	CPU ADDRESS[21]	TS IN DATA[3]
I/O2 (OPTION)	TS IN DATA[4]	CPU ADDRESS[22]	TS IN DATA[4]
RESET1	TS IN DATA[5]	CPU ADDRESS[23]	TS IN DATA[5]
CLOCK1	TS IN DATA[6]	CPU ADDRESS[24]	TS IN DATA[6]

Fig. 17

Fig. 18

ISDB-T IN JAPAN	DVB-T IN EUROPE	OPEN CABLE IN NORTH AMERICA	
		INITIAL STATE OF CABLE CARD	OPERATING STATE OF CABLE CARD
1/01	TS IN DATA[7]	CPU ADDRESS[25]	TS IN DATA[7]
NOT IN USE	TS OUT CLOCK	VS2#	VS2#
NOT IN USE	RESET	RESET	RESET
NOT IN USE	WAIT#	WAIT#	WAIT#
NOT IN USE	INPACK#	RESERVED	INPACK#
NOT IN USE	REG#	REG#	REG#
NOT IN USE	TS OUT VALID	BVD2	TS OUT VALID
NOT IN USE	TS OUT SYNC	BVD1	TS OUT SYNC
NOT IN USE	TS OUT DATA[0]	TS OUT DATA[8]	TS OUT DATA[0]
NOT IN USE	TS OUT DATA[1]	TS OUT DATA[9]	TS OUT DATA[1]
NOT IN USE	TS OUT DATA[2]	TS OUT DATA[10]	TS OUT DATA[2]
NOT IN USE	CD2#	CD2#	CD2#

*Fig. 19*

ISDB-T IN JAPAN	DVB-T IN EUROPE	OPEN CABLE IN NORTH AMERICA
Y[7]	Y[7]	Y[7]
Y[6]	Y[6]	Y[6]
Y[5]	Y[5]	Y[5]
Y[4]	Y[4]	Y[4]
Y[3]	Y[3]	Y[3]
Y[2]	Y[2]	Y[2]
Y[1]	Y[1]	Y[1]
Y[0]	Y[0]	Y[0]
UV[7]	UV[7]	UV[7]
UV[6]	UV[6]	UV[6]
UV[5]	UV[5]	UV[5]
UV[4]	UV[4]	UV[4]
UV[3]	UV[3]	UV[3]
UV[2]	UV[2]	UV[2]
UV[1]	UV[1]	UV[1]
UV[0]	UV[0]	UV[0]
H SYNC	H SYNC	H SYNC
V SYNC	V SYNC	V SYNC
CLOCK	CLOCK	CLOCK

*Fig. 20*

ISDB-T IN JAPAN	DVB-T IN EUROPE	OPEN CABLE IN NORTH AMERICA
TS1 DATA[7]	TS1 DATA[7]	TS1 DATA[7]
TS1 DATA[6]	TS1 DATA[6]	TS1 DATA[6]
TS1 DATA[5]	TS1 DATA[5]	TS1 DATA[5]
TS1 DATA[4]	TS1 DATA[4]	TS1 DATA[4]
TS1 DATA[3]	TS1 DATA[3]	TS1 DATA[3]
TS1 DATA[2]	TS1 DATA[2]	TS1 DATA[2]
TS1 DATA[1]	TS1 DATA[1]	TS1 DATA[1]
TS1 DATA[0]	TS1 DATA[0]	TS1 DATA[0]
TS1 VALID	TS1 VALID	TS1 VALID
TS1 SYNC	TS1 SYNC	TS1 SYNC
TS1 CLK	TS1 CLK	TS1 CLK
TS2 DATA[7] (OPTION)	TS2 DATA[7] (OPTION)	TS2 DATA[7] (OPTION)
TS2 DATA[6] (OPTION)	TS2 DATA[6] (OPTION)	TS2 DATA[6] (OPTION)
TS2 DATA[5] (OPTION)	TS2 DATA[5] (OPTION)	TS2 DATA[5] (OPTION)
TS2 DATA[4] (OPTION)	TS2 DATA[4] (OPTION)	TS2 DATA[4] (OPTION)
TS2 DATA[3] (OPTION)	TS2 DATA[3] (OPTION)	TS2 DATA[3] (OPTION)
TS2 DATA[2] (OPTION)	TS2 DATA[2] (OPTION)	TS2 DATA[2] (OPTION)
TS2 DATA[1] (OPTION)	TS2 DATA[1] (OPTION)	TS2 DATA[1] (OPTION)
TS2 DATA[0] (OPTION)	TS2 DATA[0] (OPTION)	TS2 DATA[0] (OPTION)
TS2 VALID (OPTION)	TS2 VALID (OPTION)	TS2 VALID (OPTION)
TS2 SYNC (OPTION)	TS2 SYNC (OPTION)	TS2 SYNC (OPTION)
TS2 CLK (OPTION)	TS2 CLK (OPTION)	TS2 CLK (OPTION)

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